



IEEE International Symposium on Intelligent Signal Processing and Communication Systems 2017

NOVEMBER 6-9, 2017, XIAMEN, CHINA

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CALL FOR PAPERS

The 2017 IEEE International Symposium on Intelligent Signal Processing and Communication Systems (ISPACS2017), sponsored by the IEEE and Huaqiao University, will be held in Xiamen—"Garden City on the Sea" in China, during 6-9 November, 2017.

ISPACS has served as a premier forum for the presentation of the fundamental research results and technological advances in the field of signal processing and communication systems. ISPACS2017 shall inherit the tradition of previous conferences in bringing together the leading engineers and scientists from around the world to escalate collaboratively the research frontier within the related areas. Prospective authors are invited to submit their original and unpublished works. Topics include, but are not limited to:

1. COMMUNICATION SYSTEMS

- 1.1 Radio Propagation and Channel Modeling
- 1.2 Communications Theory
- 1.3 Antenna and Propagation
- 1.4 Massive MIMO
- 1.5 mmWave Communications
- 1.6 Resource Allocation
- 1.7 Cognitive Radio
- 1.8 Wireless Power Transfer and Energy Harvesting
- 1.9 Intelligent Communications Systems and Network Protocols
- 1.10 Others

2. MULTIMEDIA AND SYSTEMS

- 2.1 Audio/Speech Processing and Coding
- 2.2 Image/Video Processing and Coding
- 2.3 Multimedia Communications
- 2.4 Big Data Mining
- 2.5 Internet of Things
- 2.6 Others

3. SIGNAL PROCESSING

- 3.1 Digital Filters and Filter Banks
- 3.2 Wavelets and Multirate Signal Processing
- 3.3 Non-linear Digital Signal Processing
- 3.4 Fast Computations for Signal Processing
- 3.5 Radar, Antennas Signal Processing

- 3.6 Adaptive and Array Signal Processing
- 3.7 Forensic and Security Signal Processing
- 3.8 Optical Signal Processing
- 3.9 Others

4. VLSI

- 4.1 Application-specific Digital Signal Processors
- 4.2 Low Power, Low Energy, Fault-tolerant Computing
- 4.3 Reconfigurable and Adaptive Architectures
- 4.4 Approximate and Inexact Computing
- 4.5 Heterogeneous and Multi-processor System-on-chip
- 4.6 Graphic, Video, and Multimedia DSPs
- 4.7 Modeling, Simulation, and CAD
- 4.8 Others

5. CIRCUITS AND SYSTEMS

- 5.1 Analog, Mixed-signal, and RF Circuits & Systems
- 5.2 Energy Harvesting Circuits
- 5.3 Circuits and Systems for IoT
- 5.4 Hardware and Cross-layer Security
- 5.5 Vision Sensor Network
- 5.6 Smart Devices and Wearable Electronics
- 5.7 Machine Learning and Neuromorphic Circuits
- 5.8 Others

6. EMERGING TECHNOLOGIES

Authors are invited to submit a full paper according to posted guidelines. Only electronic submissions will be accepted via the Web at: <http://ispacs2017.hqu.edu.cn>.

IMPORTANT DATES:

Deadline for Special Session Proposal	20 July 2017 27 July 2017
Deadline for Submission of Full Paper	31 July 2017 10 August 2017
Notification of Paper Acceptance	10 September 2017 15 September 2017
Deadline for Submission of Camera Ready Paper	20 September 2017 30 September 2017
Deadline for Author Registration	20 September 2017 30 September 2017